

Digital Logic Design



	memory
CHIP Xor {	program
OUT out; PARTS:	data
<pre>Not(in=a, out=nota); Not(in=b, out=notb); And(a=nota, b=b, out=w1); And(a=a, b=notb, out=w2);</pre>	Memory address input mux instruction address datess
<pre>Or(a=w1, b=w2, out=out); }</pre>	



Lecture # 25

Interfacing I/O Devices



Slides of first half of the course are adapted from: https://www.nand2tetris.org Download s/w tools required for first half of the course from the following link: https://drive.google.com/file/d/0B9c0BdDJz6XpZUh3X2dPR1o0MUE/view



@R1

D=M

M=D

1110001100001000

@temp



Today's Agenda

- How to interface I/O devices with computer
- Interfacing Screen with Hack computer
 Demo of built-in Screen chip on h/w Simulator
- Interfacing Keyboard with Hack computer
 Demo of built-in Keyboard chip on h/w Simulator
- Assembly Programming with Screen using CPU Emulator
- Assembly Programming with KBD using CPU Emulator



Input / Output



I/O Handling

- **High Level Approach:** Sophisticated software library functions are used to display text/graphics on the monitor, read the keyboard, read voice notes from mic and play the audio on speakers etc
- Low Level: Bits Manipulation

College of July attended

Interfacing I/O Devices with a Computer

- The way a microprocessor need to read/write different memory locations, similarly the microprocessor also need to read/write different I/O devices like the keyboard, mouse, monitor, printer, etc. This linking is also be called I/O Interfacing. An I/O interface acts as a communication channel between the processor and the externally interfaced device. The interfacing of the I/O devices can be done in two ways
 - Memory Mapped I/O Interfacing: Both memory and I/O devices have same address space. So addressing capability of memory become less because some part is occupied by the I/O. In memory mapped I/O, there are same read-write instructions for memory and I/O devices, so CPUs are cheaper, faster and easier to build. Example is Hack CPU
- Isolated I/O Interfacing: The I/O devices are given a separate addressing region (separate from the memory). These separate address spaces are known as 'Ports'. In isolated I/O, there are different readwrite instructions for memory and I/O devices. x86-64 use Isolated I/O
 Note: Data can be transferred between CPU and I/O devices in three modes, namely Program controlled I/O, Interrupt initiated I/O, and Direct Memory Access



Interfacing Screen with Hack Computer



Memory Mapped Output



Screen Memory Map:

- Screen memory map is a designated memory area, dedicated to manage a display unit
- To write something on the display unit, write some bits in the designated memory area (zero to make a pixel off/white and one to make a pixel on/black)
- The physical display is continuously *refreshed* from the contents of memory map, many times per second
- Whatever, we write in the memory map makes the corresponding pixels of screen black and white in the next refresh cycle
- This is how we can write "Hello World" message on the screen









- The physical screen is of 256 rows and 512 columns which makes $256 \times 512 = 131072$ pixels
- To map each pixel of screen on a single bit, the Screen memory map must contain 8K, 16 bits words, which makes $8192 \times 16 = 131072$ bits
- The built-in chip implementation has the side effect of continuously refreshing a visual 256 by 512 black-and-white screen, simulated by the simulator. Each row in the visual screen is represented by 32 consecutive 16-bit words, starting at the top left corner of the visual screen.



Screen Built-in Chip









Interfacing Keyboard with Hack Computer



- The physical keyboard requires just one word inside the Hack Memory, as it will contain the ASCII code of the character pressed on keyboard.
- So the 16 bit word of Hack RAM at address 24576 is where the keyboard is mapped.



The Hacker Character Set

key

0

1

•••

9

•

;

<

=

>

?

@

key	code	
(space)	32	
!	33	
	34	
#	35	
\$	36	
%	37	
&	38	
د	39	
(40	
)	41	
*	42	
+	43	
ر	44	
-	45	
•	46	
/	47	

code	key	cod
48	А	65
49	В	66
	C	
57		
-0	Z	90
58		
59	[91
60	/	92
61]	93
62	^	94
63	_	95
64	``	96

code

key	code
а	97
b	98
с	99
z	122
{	123
I	124
}	125

~

126

key	code
newline	128
backspace	129
left arrow	130
up arrow	131
right arrow	132
down arrow	133
home	134
end	135
Page up	136
Page down	137
insert	138
delete	139
esc	140
f1	141
f12	152





When a key is pressed on the keyboard, the key's scan code appears in the keyboard memory map. Since no key is being pressed on the keyboard in this figure, so the keyboard memory map contains all zeros

To check which key is currently pressed:

- Probe the contents of the Keyboard chip
- In the Hack computer: probe the contents of RAM[24576]



Memory Mapped Input



When a key is pressed on the keyboard, the key's scan code appears in the keyboard memory map

To check which key is currently pressed:

- Probe the contents of the Keyboard chip
- In the Hack computer: probe the contents of RAM[24576]



Memory Mapped Input



When a key is pressed on the keyboard, the key's scan code appears in the keyboard memory map

To check which key is currently pressed:

- Probe the contents of the Keyboard chip
- In the Hack computer: probe the contents of RAM[24576]



Keyboard Built-in Chip











Hack Assembly Programming involving I/O on Hack CPU Emulator



I/O Devices: Screen And Keyboard

CPU Emulator (1.4b3)
e <u>Vi</u> ew <u>R</u> un <u>H</u> elp
Animate: Vier Slow Fast Program flow S Simulated screen: 256 columns by 512
Image: Constraint of the second se



I/O Devices: Keyboard in Action





I/O Devices: Keyboard in Action

nate

ram flow

View:

Screen

🖆 CPU Emulator (1.4b3) File <u>View Run Heln</u>

Perspective: That's how computer programs read from the keyboard: they peek some keyboard-oriented memory device, one character at a time.

This is rather tedious in machine language programming, but quite easy in high-level languages that handle the keyboard indirectly, using OS routines like readLine or readInt, as we will see in Chapters 9 and 12.

Since all high level programs and OS routines are eventually translated into machine language, they all end up doing something like this example.



Format:

Decimal

Script restarted

20

21 22

23

map

Keyboard memory

(a single 16-bit

memory location)

Instructor: Muhammad Arif Butt, Ph.D.

А



I/O Devices: Screen in Action

Perspective: That's how computer programs put images (text, pictures, video) on the screen: they write bits into some display-oriented memory device.

This is rather hard to do in machine language programming, but quite easy in high-level languages that write to the screen indirectly, using OS routines like **printString Or drawCircle**, as we will see in chapters 9 and 12.

Since all high level programs and OS routines are eventually translated into machine language, they all end up doing something like this example.







Hack Assembly for Input & Output







Pseudo code

for (i=0; i<50; i++)			
draw 16 black pixels at the beginning of row i	RAM	🗋 🛤	
	16370 16371	0	
	16372	0	
addr = 16381	16373	0	
adur - 10304	163/4	0	
n = DAM[0]	16376	0	
n = RAM[0]	16377	0	
i = 0	16378	0	
1 - 0	16379	0	physical
	16380	0	screen
	16382	ő	
LOOP	16383	0	
2001 .	16384	-1	
if i > n goto END	16385	0	
	16387	ő	
RAM[addr] = -1 //11111111111111111111111111111111		0	
// advances to the next year	16 black pixels,	0	D
// advances to the next row	corresponding to	0	
addr = addr + 32	the first row of	0	screen
i = i + 1	the rectangle	0	Serven
	16205	0	memory ut:
goto LOOP	16395	0	map 1 —
	16397	ő	M/A Input :
	16398	0	27 —
END:	A	27	
goto END			

Draws a filled rectangle at the screen's (LOOP)	
top left corner, with width of 16 pixels Qi	
and height of RAM[0] pixels. D=M	
Usage: put a non-negative number	
(rectangle's height) in RAM[0] */	
(END (END	
D;JGT // if i>n goto END	
0 addr	
M=D //n = RAM[0] M=-1 //RAM[addr]=11111111111111111111111111111111111	L1
Qi Qi	
M=0 // i = 0	
M = M + 1 / / 1 = 1 + 1 0.32	
D=A	
D=A @addr	
M = D + M // addr = addr + 32	
M=D // addr = 16384 (screen's base @LOOP	
0; JMP // goto LOOP	
(END)	
(LOOP) @END // program's end	
// 0;JMP // infinite loop	





Aufait United to the second se

Example 2: fill.asm





Example 2: fill.asm

			CPU Em	nulator	(2.5) -	/Users	arif/Documents	s/01 Arif-0	CS223-CO/	AL/Lectu	reSlides	-Video S	essions	/Lecture	e Codes/	21/Fill.asm	ı			
ile	View	Run	Help																	
	>					1	Slow Fast	Animate No a	e: nimation	View S	/: cr 🗘	Format D	:							
ROM 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42	 	P IP 1 76 D 1 1 T				RAM 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28		18432 0		ALU Dir M/A	J put:	D 6144 20		Whe	6144 en ar Is of	*+++++++++++++++++++++++++++++++++++++	y is en	s pres	ssed	a
P	с		20			Α		20						blac	k					

Running...



Fill: A Simple Interactive Program





